

Figure 1

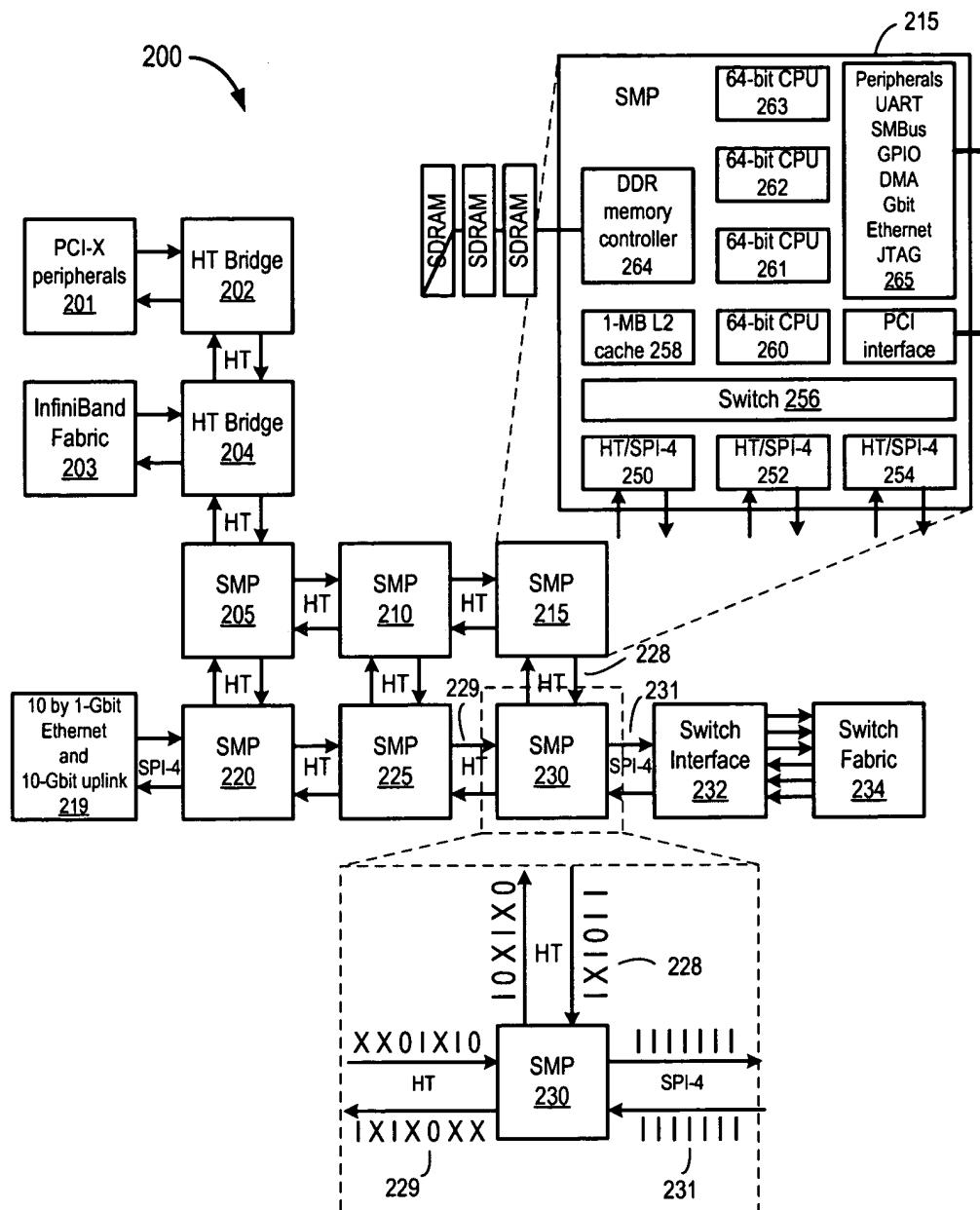


Figure 2

Docket No. BP 3255.3

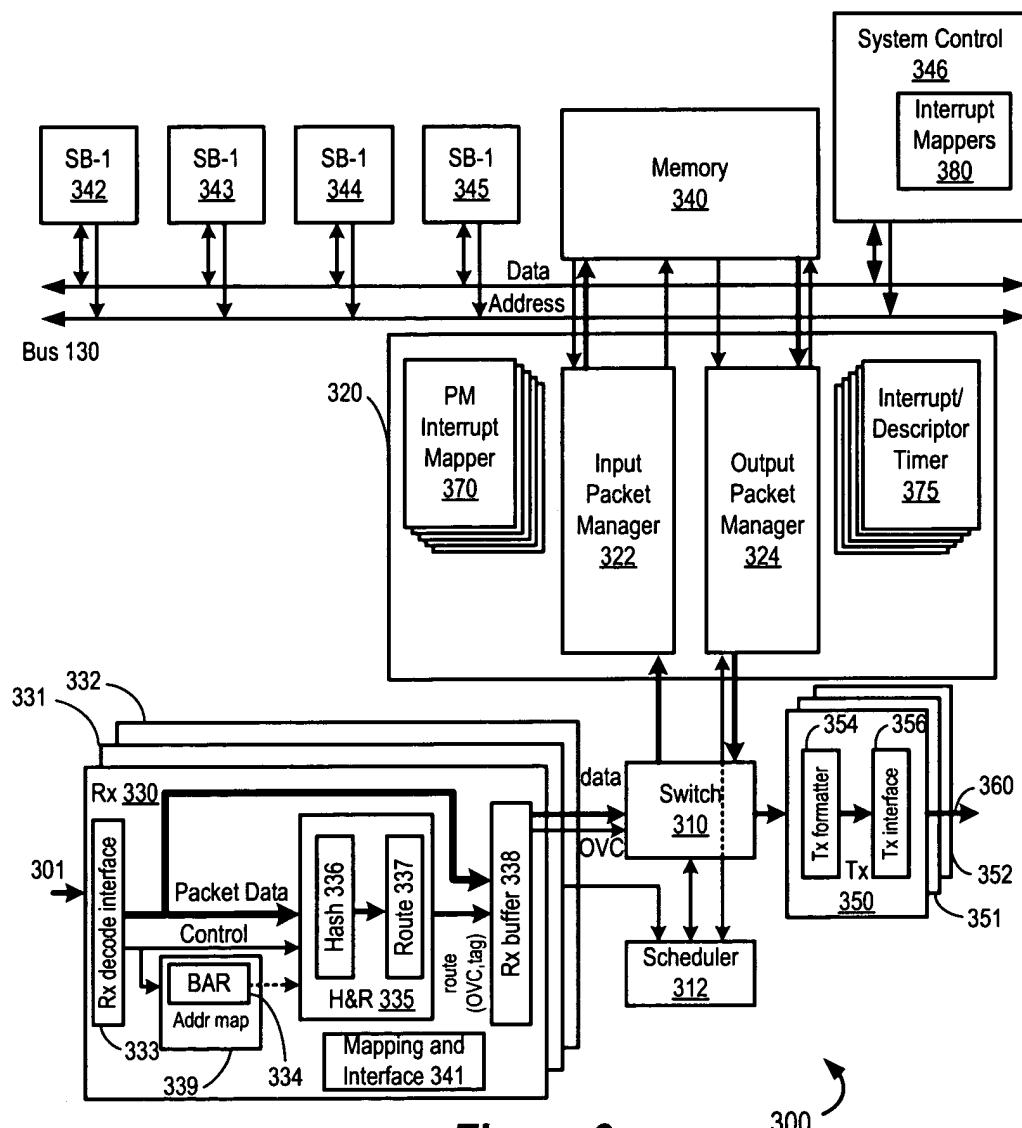


Figure 3

Docket No. BP 3255.3

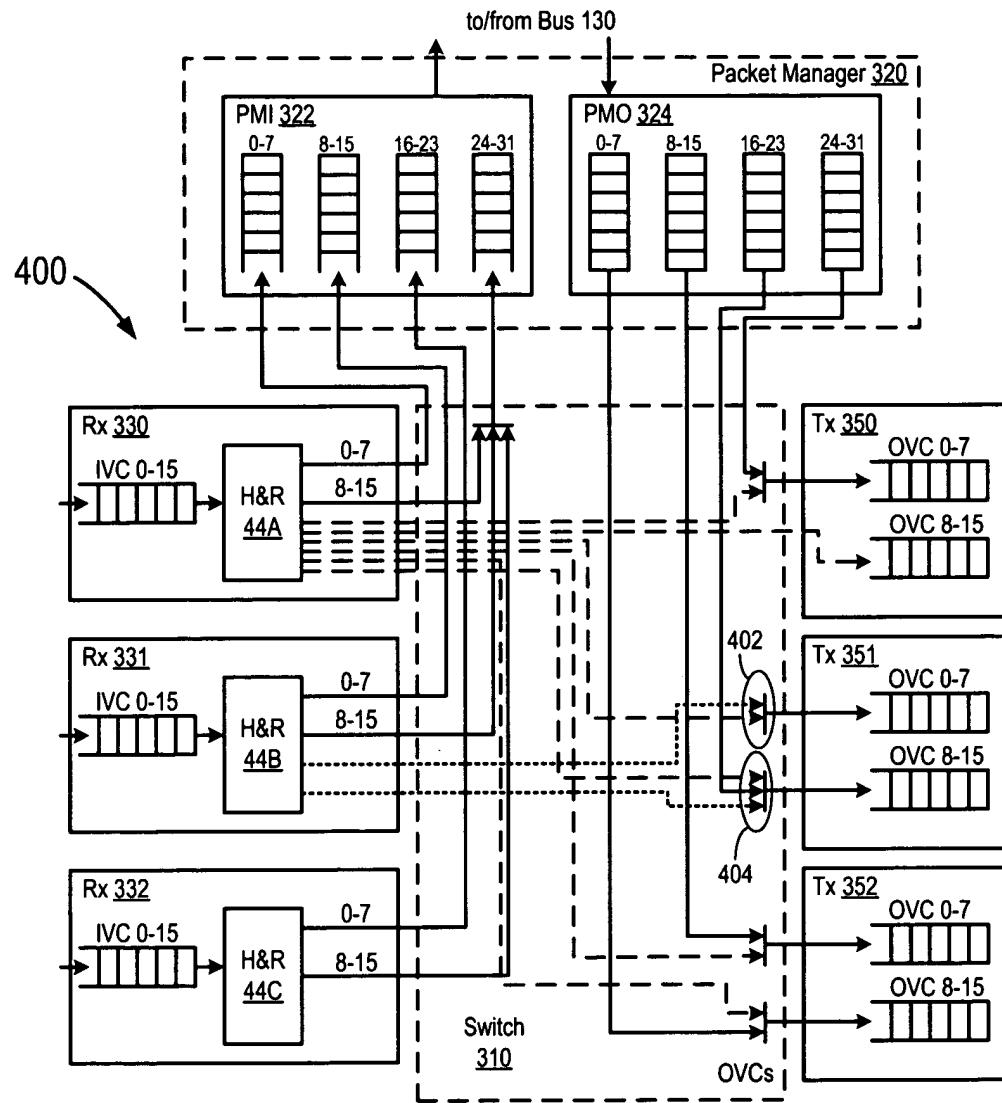


Figure 4

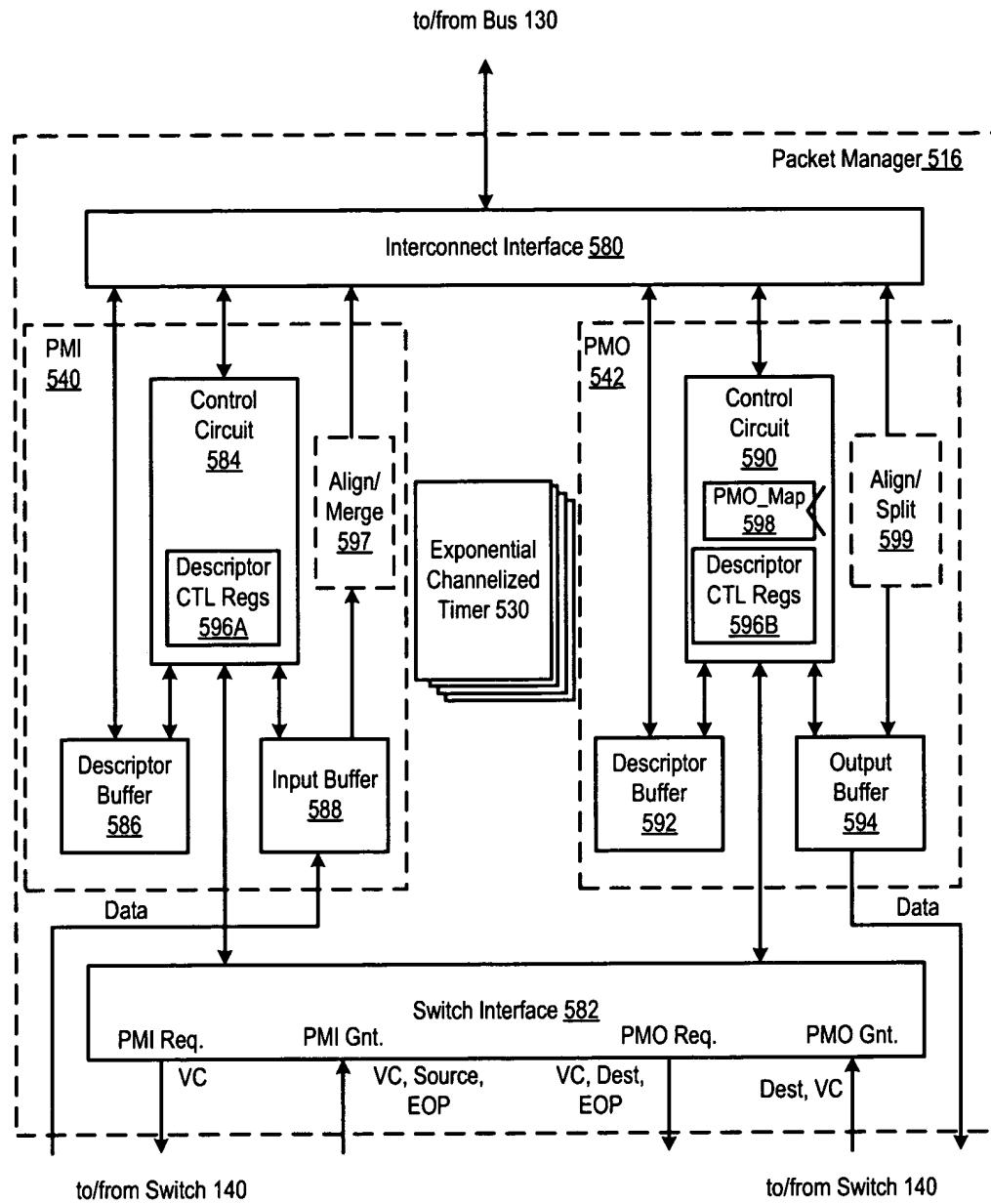


Figure 5

Docket No. BP 3255.3

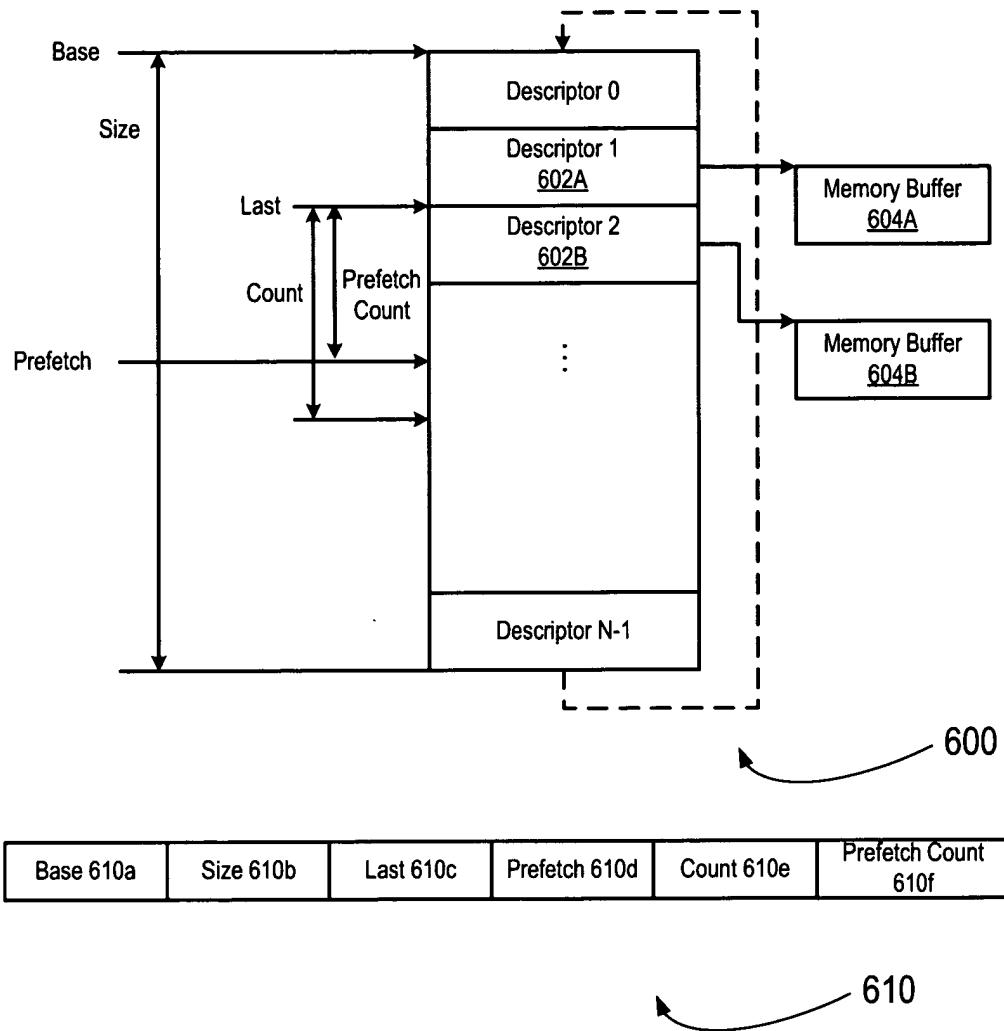
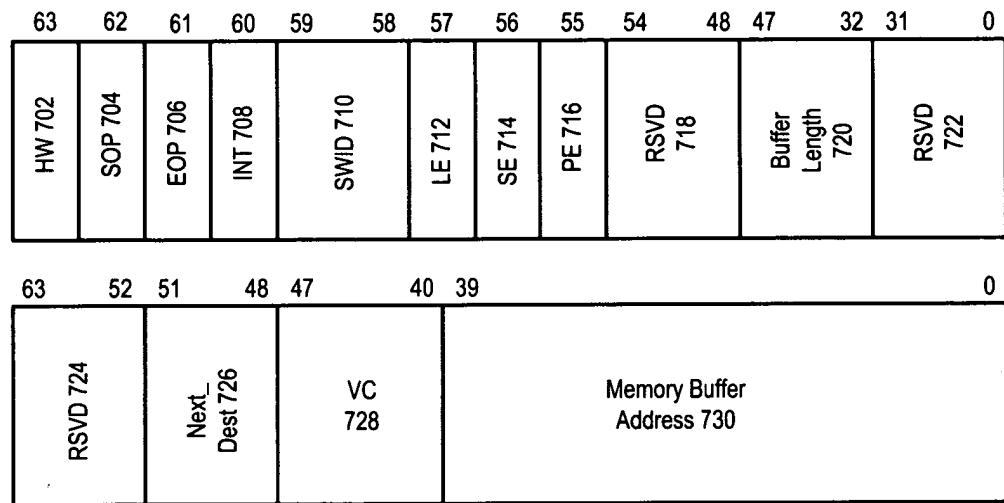


Figure 6



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Figure 7

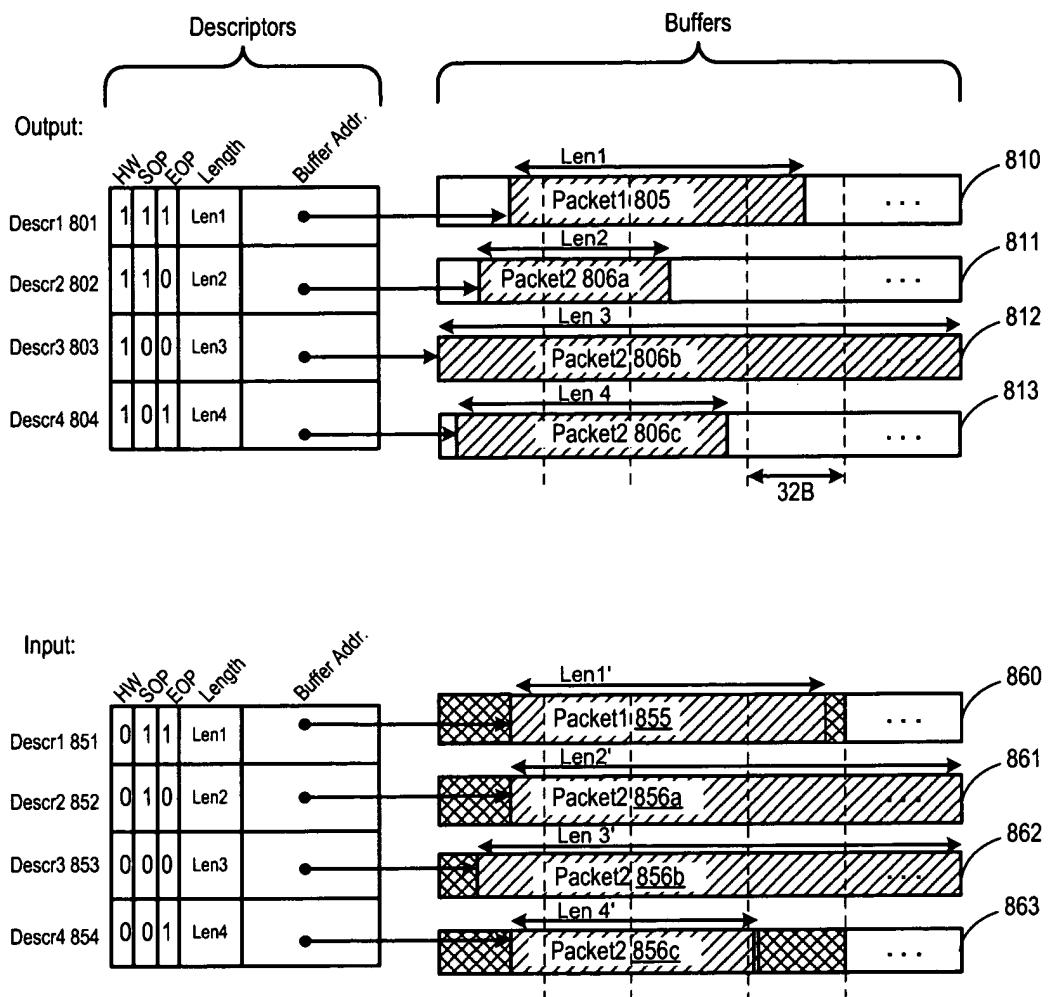


Figure 8

	<u>Hardware Bit</u>	<u>SOP Bit</u>	<u>EOP Bit</u>	<u>Memory</u>	<u>Packets Received</u>	<u>Processors Assigned</u>	<u>Working</u>
900	0	0	0	IQ Descriptor			
901	0	0	0	IQ Descriptor			
902	1	1	0	IQ Descriptor	Packet 0	CPU0	CPU0
903	1	0	1	IQ Descriptor		CPU0	CPU0
904	1	1	0	IQ Descriptor	Packet 1	CPU0	CPU0
905	1	0	1	IQ Descriptor		CPU0	CPU0
906	1	1	0	IQ Descriptor		CPU1	CPU1
907	1	0	0	IQ Descriptor	Packet 2	CPU1	CPU1
908	1	0	1	IQ Descriptor		CPU1	CPU1
909	1	1	0	IQ Descriptor		CPU1	CPU2
910	1	0	0	IQ Descriptor	Packet 3	CPU2	CPU2
911	1	0	0	IQ Descriptor		CPU2	CPU2
912	1	1	1	IQ Descriptor	Packet 4	CPU2	CPU2
913	1	1	0	IQ Descriptor		CPU2	CPU3
914	1	0	1	IQ Descriptor	Packet 5	CPU3	CPU3
915	1	1	1	IQ Descriptor		CPU3	CPU3
916	1	1	0	IQ Descriptor	Packet 6	CPU3	CPU3
917	1	0	0	IQ Descriptor		CPU3	CPU3
918	0	0	0	IQ Descriptor	Packet 7		
919	0	0	0	IQ Descriptor			
920	0	0	0	IQ Descriptor			

Finished Descriptors

Figure 9

Docket No. BP 3255.3

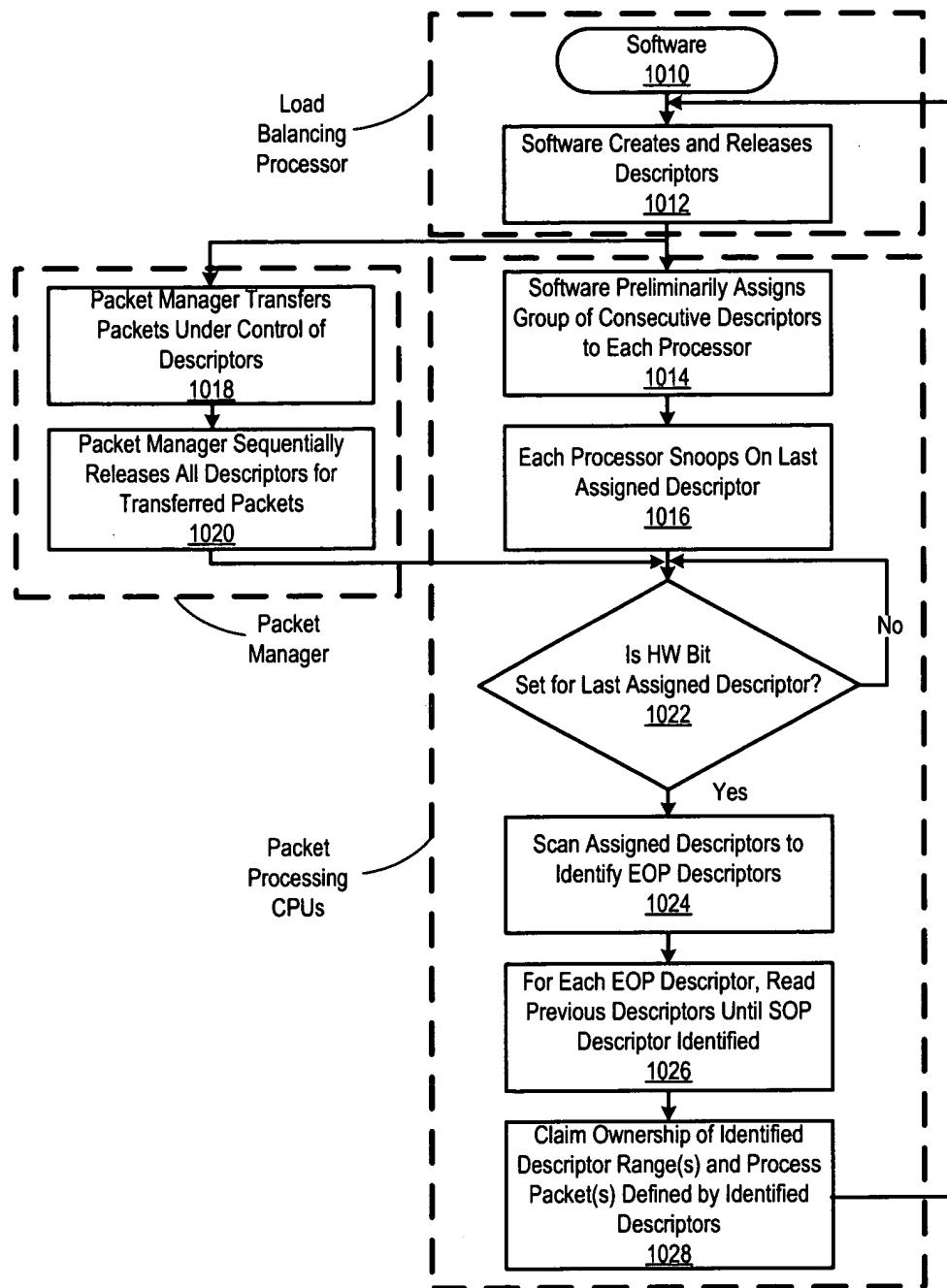


Figure 10